



CALL FOR PAPERS

ICEEDLE 2020
Jan 09-10, 2020
Singapore, SG

The International Research Conference is a federated organization dedicated to bringing together a significant number of diverse scholarly events for presentation within the conference program. Events will run over a span of time during the conference depending on the number and length of the presentations.

ICEEDLE 2020 : International Conference on Electrical Engineering and Digital Logic Expressions is the premier interdisciplinary forum for the presentation of new advances and research results in the fields of Electrical Engineering and Digital Logic Expressions. The conference will bring together leading academic scientists, researchers and scholars in the domain of interest from around the world. Topics of interest for submission include, but are not limited to:

Employ simple lumped circuit models for resistors, sources, inductors, capacitors, and transistors in circuits
Analyze circuits made up of linear lumped elements
The frequency range of simple electronic circuits
The semiconductor concepts of drift, diffusion, donors and acceptors
Algebra to describe the function of logic circuits
Design circuits which represent digital logic expressions

Designing simple devices and circuits to meet stated operating specifications
Check static discipline constraints in circuits
Determine the output produced by a circuit
Time behavior of first order and second order capacitors, inductors and circuits containing resistors
Construct simple gates, amplifiers, or filters in the laboratory