



CALL FOR PAPERS

ICTLD 2021
Jan 18-19, 2021
Rome, Italy

The International Research Conference is a federated organization dedicated to bringing together a significant number of diverse scholarly events for presentation within the conference program. Events will run over a span of time during the conference depending on the number and length of the presentations.

ICTLD 2021 : International Conference on Transistor Level Design is the premier interdisciplinary forum for the presentation of new advances and research results in the fields of Transistor Level Design. The conference will bring together leading academic scientists, researchers and scholars in the domain of interest from around the world. Topics of interest for submission include, but are not limited to:

Transistor level design
Design of digital integrated circuits
Chip layout
Design rules
Circuit-level modeling and optimization
Transistor level circuit analysis

Transistor level implementation
Transistor level verification
Transistors and logic gates
Simulation of logic gates and subsystems
Transistor level evolution of digital circuits